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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3239	714/724.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/13 11:09
L2	838	714/733.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/13 11:10
L3	1273	714/738.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/13 11:10
L4	220	714/742.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/13 11:13
L5	1282	324/73.1.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/13 11:16
L6	451	326/16.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/13 11:16
L7	795	326/21.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/13 11:18
L8	1704	714/25.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/13 11:18
S1	7	(JTAG adj tap) and master and ((microprocessor or processor) adj interface) and ((integrated adj circuit) or IC or chip\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/13 11:07
S5	3	(core adj logic) with (JTAG adj TAP)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/10 11:08

S9	4	(core adj logic) and (jtag adj tap) and (jtag adj master)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/10 11:48
S10	5	(jtag adj tap) and (jtag adj master)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/10 11:52